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APPLICATION NO.	FILING DATE	FIRST NAMED INVENTOR	ATTORNEY DOCKET NO.	CONFIRMATION NO.
10/756,756	01/14/2004	Hajime Kimura	12732-207001 / US6910	1526
26171 7590 11/14/2008 FISH & RICHARDSON P.C. P.O. BOX 1022 MINNEAPOLIS, MN 55440-1022				
EXAMINER PIZIALI, JEFFREY J				
ART UNIT 2629		PAPER NUMBER		
NOTIFICATION DATE 11/14/2008		DELIVERY MODE ELECTRONIC		

Please find below and/or attached an Office communication concerning this application or proceeding.

The time period for reply, if any, is set in the attached communication.

Notice of the Office communication was sent electronically on above-indicated "Notification Date" to the following e-mail address(es):

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APPLICATION NO./ CONTROL NO.	FILING DATE	FIRST NAMED INVENTOR / PATENT IN REEXAMINATION	ATTORNEY DOCKET NO.
10756756	1/14/04	KIMURA ET AL.	12732-207001 / US6910

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EXAMINER

Jeff Piziali

ART UNIT	PAPER
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2629 20081106

DATE MAILED:

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Commissioner for Patents

The reply filed on 25 August 2008 is not fully responsive to the prior Office action because of the following omission(s) or matter(s):

The election (filed on 25 August 2008) states, "Responsive to the action mailed July 24, 2008, applicant elects the invention of Species 2, drawn to a first current source circuit configuration, as shown in Fig. 3(A) and pages 9-10 of the specification."

However, Species 1 (not Species 2) is actually, "drawn to a first current source circuit configuration, as shown in Fig. 3(A) and pages 9-10 of the specification."

As detailed in the restriction requirement mailed on 24 July 2008 (see page 2):

Species 1 is drawn to the first current source circuit configuration [fig. 3(A) and pages 9-10 of the specification], whereas Species 2 is drawn to the second current source circuit configuration [fig. 3(B) and pages 10-11 of the specification].

The Applicants are respectfully requested to clarify whether they are intending to elect:

Species 1, drawn to a first current source circuit configuration [e.g., see fig. 3(A) and pages 9-10 of the specification], or Species 2, drawn to a second current source circuit configuration [e.g., see fig. 3(B) and pages 10-11 of the specification].

See 37 CFR 1.111. Since the above mentioned reply appears to be bona fide, applicant is given a TIME PERIOD of ONE (1) MONTH or THIRTY (30) DAYS from the mailing date of this notice, whichever is longer, within which to supply the omission or correction in order to avoid abandonment. EXTENSIONS OF THIS TIME PERIOD MAY BE GRANTED UNDER 37 CFR 1.136(a).

/Jeff Piziali/
Primary Examiner, Art Unit 2629
6 November 2008